

CONTEXT SCHEDULING

ABSTRACT

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10 A programmable processing system that executes multiple
instruction contexts includes an instruction memory for
storing instructions that are executed by the system, fetch
logic for determining an address of an instruction, with the
fetch logic including scheduling logic that schedules
execution of the instruction contexts based on condition
signals indicating an availability of a hardware resource,
with the condition signals being divided into groups of
condition signals, which are sampled in turn by the scheduling
15 logic to provide a plurality of scan sets of sampled
conditions.